



Specifications

power XED Super Orange

Part number: LX-01PXX-SO-00-00 *)









*) Note: Description of part number refer to page 13.

Rev.02 (04/09)



UNIQUE FEATURES

- Proprietary Chip-in-Silicon [CiS] technology
- Silicon (Si-wafer) generic package
- Miniature form factor: 2.55mm x 2.55mm x 0.6mm
- Best-in-class Thermal Resistance: 12 K/W
- Suitable for assembly on standard FR4 and MC PCB

DESIGNED & SPECIFIED FOR

- Automotive exterior & interior lighting
- Medical & dental lighting
- Transportation equipment interior lighting (aircraft, train, vessels)
- Emergency luminaries
- Backlight light source for LCD displays
- Decorative and architectural lighting
- Accent lighting
- Industrial equipment lighting

. Luminous Flux: 35 Lumen

. Standard SMT assembly

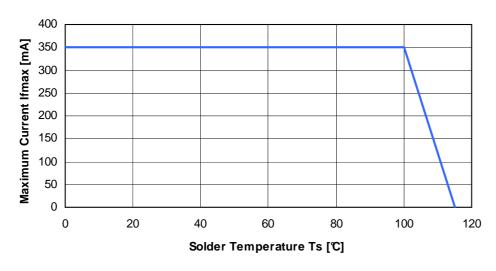


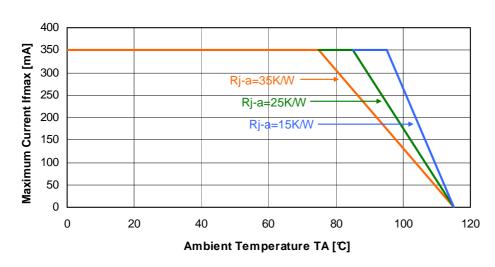
Absolute Maximum Ratings

Ta=25℃

Parameter	Symbol	Value	Unit
Power Dissipation	P_D	1	W
DC Forward Current	I _F	350	mA
Pulsed Forward Current (10ms pulse width of duty 10%)	I _{FP}	350	mA
Operating Temperature	T_{op}	-40 ~ +85	Q
Storage Temperature	T _{stg}	-40 ~ +85	Ĉ
Junction Temperature	T_j	115	Ç

Allowable Forward Current vs. Solder / Ambient Temperature based on max. Ti







Electro-Optical Characteristics



T_a=25℃

T _a =23 C						
Parameter	Symbol	Condition	min	typ	max	Unit
Forward Voltage	V _F	I _F =350 mA	-	2.5	3.0	V
DC Forward Current	I _F			350	350	mA
Viewing Angle	20%	I _F =350 mA		130	-	deg
Dominant Wavelength	λ_d	I _F =350 mA	610	615	620	nm
Luminous Flux	ϕ_{V}	I _F =350 mA		30		lm
R _{thjs}		I _F =350 mA		12		K/W
Optical efficiency		I _F =350 mA		34		lm/W

Ranking		$I_F=350 \text{ mA}, T_a=25\%$
Rank	Luminous F [lm]	Flux φ ∨
	Min.	Max.
Α	25	35
В	35	

Ranking $I_F=350 \text{ mA}, T_a=25^{\circ}\text{C}$

- 7			
	Rank	Luminous Inte [cd]	nsity I $_{\vee}$ *)
		Min.	Max.
	Α	6,4	9,8
	В	9,8	

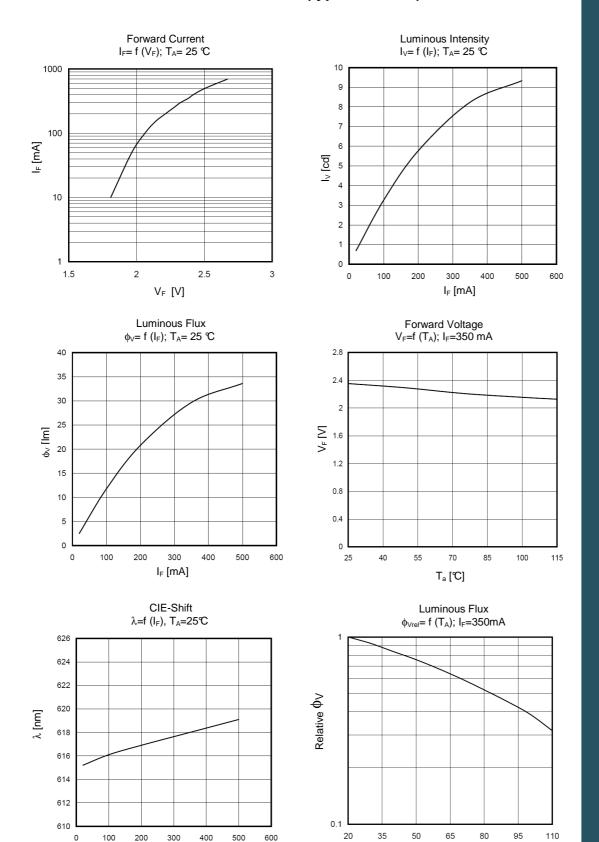
*) Note: XEDs have a 100% final testing procedure in the production. The measurement includes the luminous intensity (cd) and dominant wavelength (current pulse duration 10ms). Tolerance ranges: I_V - \pm 10% λ_{D} - $\pm~$ 1nm These measured values are basis for the release of the



delivery.



Reference Curves (typical values)

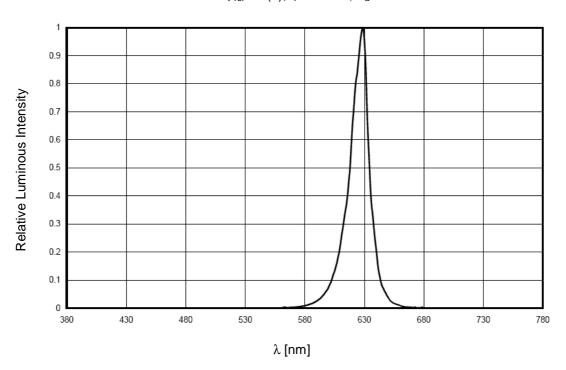


T_S [℃]

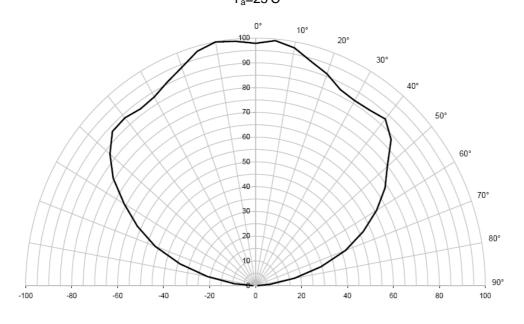
I_F [mA]



Relative Spectral Emission *) $I_{v-rel} = f(\lambda)$; $I_F=350mA$, $T_a=25^{\circ}C$



Radiation Characteristic T_a=25℃



Relative Intensity [%]

*) Note:

Measurement equipment: Integrating sphere (inner diameter: 500mm) Measurement tolerance: ± 0,01



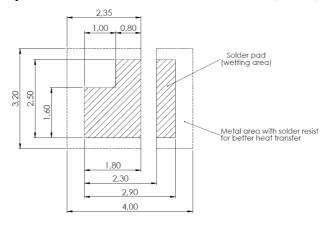


Soldering Guidelines

- Soldering paste and optical layer must not come into contact. This could affect the performance of the component.
- Do not exert external pressure on any part of the resin when soldering.
- The component should be cooled to room temperature before further processing.
- This product is not suitable for wave soldering. Use re-flow solder or use a hot plate when soldering.
- Soldering under N2-atmosphere is recommended to maintain reliability.
- Soldering rework is not recommended.
- Do not implement multiple re-flow methods.
- After soldering, do not bend the circuit board.

Solder Pad / Footprint

(Unit:mm)



Recommended dimensions

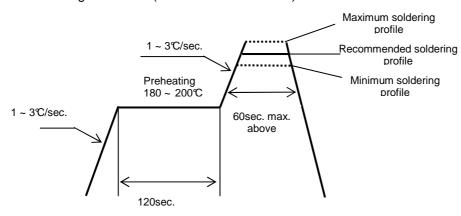
Note: The solder pad should be close to the electrode area, as recommended.

The larger the metal area with solder resist, the better the heat transfer.

Reflow Soldering Conditions / Profile

- Preheat to be at maximum 200℃ for maximum 2 minute s.
- Soldering temperature should not exceed 260℃ for m aximum 10 seconds.

Recommended soldering conditions (surface of circuit board) -lead-free solder







- Please check the mounting reliability based on the application.
- Excessive coating of solder paste on the board may result in the formation of a "Solder Ball" on the board and/ or on the side surface of the XED after reflow. Customer should take note that the "Solder Ball" may cause a short circuit.
- A too large solder pad area can cause the XED to move out of place. Therefore it is our recommendation that only our recommended footprint with an adapted solder paste quantity should be used.

Thermal Management

- Thermal design of the end product is critical to the performance of the XED. The heat generation of the XED must be considered when making the system design.
- Operate XEDs within the maximum ratings given in this specification.
- Please choose only an operating current according to XED's ambient temperature given in this specification and connect the XED to a suitable cooling system.
- The equation below indicates correlation between T_J and T_A.

$$T_J = T_A + R_{th JA} * P$$

Definitions and Abbreviations:

 T_J = Junction Temperature: [\mathbb{C}] T_A = Ambient Temperature: [\mathbb{C}]

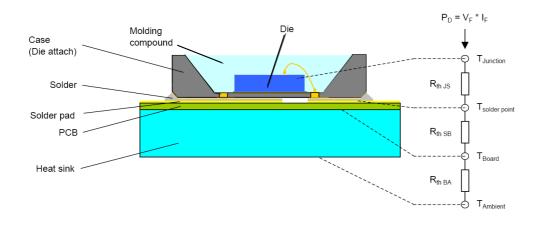
R_{JA} = Thermal resistance from XED Junction to Ambient: [K/W]

P = Power Input (IF * VF) : [W]

Typical Thermal Resistance Junction-Solder Point

$$R_{th JS}$$
 typ. = 12 K/W

Schematic diagram of thermal package







General Guidelines

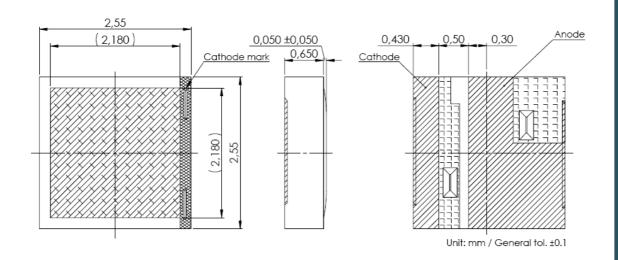
- The XEDs described in this brochure are intended for ordinary electronic equipment such as office equipment, communications equipment, measurement instruments and household appliances. Consult LEXEDIS sales staff in advance for information on the applications in which exceptional quality and reliability are required, particularly when the failure or malfunction of the XEDs may directly harm life or health (such as for airplanes, aerospace, submersible repeaters, nuclear reactor control systems, automobiles, traffic control equipment, life support systems and safety devices).
- User shall not reverse engineer by disassembling or analysis of the XEDs without having prior written consent from LEXEDIS. In the rare occurrence that XEDs are found faulty, the user shall inform LEXEDIS directly before disassembling or analysis.
- The formal specifications must be agreed and signed by both parties prior to large volume purchase begins.

The appearance and specifications of the product may be modified for improvement without prior notice.





Outline Dimensions



Item	Material
Package	Silicon (Si)
Encapsulating	Silicone and Phosphor

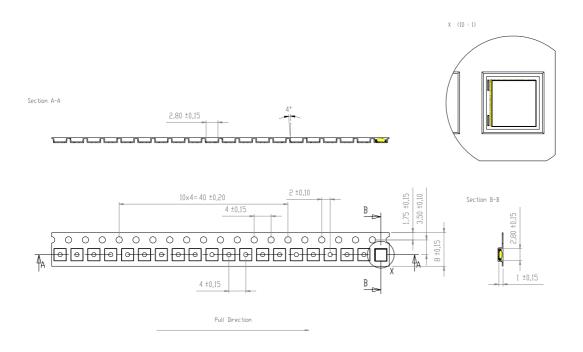
Packing

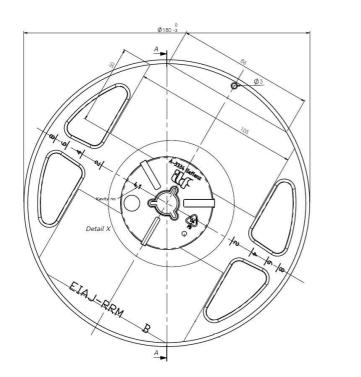
- Quantity is 2000 pcs/reel.
- The reel is packed in an antistatic plastic pack.
- The XEDs are packaged in cardboard boxes for transportation.
- The packages should be handled carefully to avoid damage of the components.
- The boxes are not waterproof and therefore must be protected from water and moisture.
- When the XEDs are transported, we recommend using the same packing method as LEXEDIS.





Reel Packing Structure (Reel ϕ 180mm)







A-A

/3 B

В

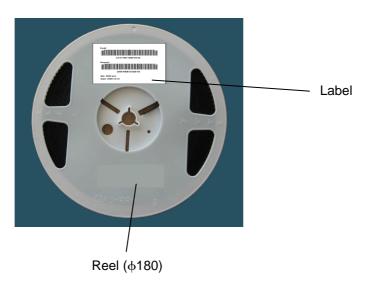


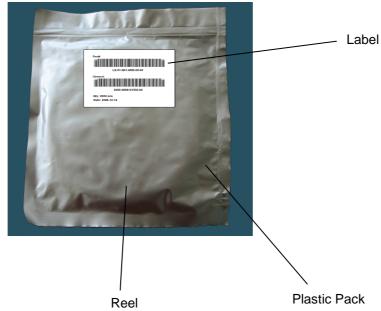
Labeling

The label on the packing units shows the material number, lot number, date code and quantity.



Label positions







Description Part Number and Label



Material number:

Lexedis' internal SAP-number

Part description:

Generated out of "material number" via decoder-chart

Part#		Sinaxar		Revisio	Level		Product	Intensit	y Rank		Colour		Colour		June 1	Lens		Packing	
Option:	L	X	-	0	1	-	N	0	1	-	3	0	W	-	0	0	-	0	0
Option:							Х				3	5	W		3	5			
Option:							Р				4	2	W		5	0			
Option:											5	7	W		6	5			
Option: Option: Option:											6	5	W						
Option:													R						
Option:													G						
Option:													В						
Position:	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19

Legend part description:

Position:		Descriptio	n:								
		Descriptio	П.								
1 - 2	=	Lexedis									
4 - 5	=	Revision N	Revision Number								
7	=	Product:	N = nano)	KED							
		•	X = XEDo	n							
			P = Powe	rXED							
8 - 9	=	intensity ra	ank:	01 = Rank A							
		•		02 = Rank B							
				03 = Rank C							
11 - 12	=	colour ran	ge white:	30 = 3000K							
		•		35 = 3500K							
				42 = 4200K							
				65 = 6500K							
13	=	colour:	W = white								
		•	B = blue								
			R = red								
			G = green	١							
15 - 16	=	lens:	00 = no le								
			35 = 35° l	ens							
			50 = 50° l	ens							
	ens										
18 - 19	=	type of packaging									
			00 = blister								
		·	11 = on PCB								
		99 = bulk									

Lot-number:

Identification-number for SAP-data

Date-code:

ganaral		Ous	ntitur			date							Operator:				
general:	Quantity:					Υ	Υ	M	M	ם	D		Operator.				
Option:	2	0	0	0	-	0	7	1	0	1	4	-	3	3	5	0	7
Option:																	
Position:	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17





Precautions in Handling

Safety Precautions

- The XED light output is intense enough to cause injury to human eyes if viewed directly. Precautions must be taken to avoid looking directly at the XEDs with unprotected eyes [according IEC 60825-1 (EN 60825-1)].
- Condensation on the module has to be avoided.
- Contact with acid solvents must be avoided.

Protection against electrostatic discharge - ESD

XEDs are electronic components and sensitive to electrostatic discharge. Appropriate ESD protection measures must be taken when working with XED products. For example, earthed shoes or ESD wristbands have to be applied. Non-compliance with ESD protection measures may lead to damage or destruction of the product.

Precaution in driving

Products are designed exclusively for forward current driving. Please avoid driving system with reverse voltage, which may cause migration which damages the product.

Baking

- If the moisture absorbent material (silica gel) is no longer effective or the products have exceeded the storage time, baking treatment should be performed under the following condition:
- Baking treatment : more than 12 hours at 60±5℃.

Cleaning

Chemical solvents or cleaning agents must not be used to clean the XED component. Mechanical stress on the XED component must be avoided. It is best to use a soft brush, damp cloth or low-pressure compressed air.

Storage

- XEDs should ideally be stored in as-delivered condition and in the original packaging. The products should be stored away from direct light in dry location.
- After opening the package: The XEDs should be kept at 30℃ or less and 70%RH or less. The XEDs should be soldered within 12 hours after opening the package. If unused XEDs remain, they should be stored in the moisture proof packages, such as sealed containers with packages of moisture absorbent material (silica gel). It is also recommended to return the XEDs to the original moisture proof bag and to reseal the moisture proof bag again.
- Please avoid rapid transitions in ambient temperature, especially in high humidity environments where condensation could occur.





Revision

REVISION 02		
Date	Description	Page
2008-12-09	"designed & specified for" supplemented	2
2008-12-09	new derating curves implemented	3
2008-12-09	R _{this} & optical efficiency implemented	4
2008-12-09	new solder pad/footprint	7
2008-12-09	new outline dimension	10
2009-04-28	new rank B & C	4
2009-04-28	Insert optical efficiency	4
2009-04-29	Insert safety causions pt.2 & pt.3	15







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Company information:

LEXEDIS Lighting GmbH, a joint venture company between TridonicAtco of Austria and Toyoda Gosei of Japan, invents and produces innovative and high-performance digital light sources marketed worldwide under the XED brand. Founded in 2005, LEXEDIS is a fast-growing and customer-oriented company providing advanced opto-semiconductor products for the advancement of energy-efficient lighting solutions in automotive, general lighting, industrial and display applications. As the creator of XED technology, LEXEDIS is the leading company in the development of intelligent light sources.

For technical assistance or the location of your nearest customer-service office contact the following:

LEXEDIS Lighting GmbH

Technologiepark 10
A-8380 Jennersdorf
Austria
Tel No:0043-(0)5-9010-650-00
www.lexedis.com
info@lexedis.com

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