

# **Oscillator Specification: E3222**

Issue 2, 20<sup>th</sup> October 2004, LN2443

# Outline in mm

#### **Pad Connections**

- 1. Control Voltage (Vc)
- 2. GND
- 3. Output
- 4. Supply Voltage

## Marking

- CMAC
- Manufacturing identifier (XX)
- Pad 1 / Static sensitivity identifier ( )
- Part number (four digits)
- Device date code (YW)



Recommended PCB patern



# Electrical

Nominal Frequency, Fo Supply Voltage, Vs Input Current Output: Type Load Level Duty-Cycle at 50% amplitude level

#### Frequency Stability

Calibration Tolerance at Vc=1.5V, 25°C after Reflow soldering Temperature, -20°C to 85°C Supply Voltage, ± 5% Ageing, first year at 35°C Ageing, 7 years at 35°C 14.4 MHz 2.8 V  $\pm$  5%  $\leq$  2.5 mA (typ. 2.0 mA)

Clipped Sinewave, DC-coupled 2 k $\Omega$  // 20 pF  $\geq$  0.7 Vpk-pk 40 to 60%

 $\leq \pm 1.5 \text{ ppm}$  $\leq \pm 1.0 \text{ ppm } [\pm(\text{Fmax-Fmin})/2\text{Fo}]$  $\leq \pm 0.1 \text{ ppm reference to frequency at 2.8V}$  $\leq \pm 1.0 \text{ ppm}$  $\leq \pm 3.0 \text{ ppm}$ 

Start-up time, > 90% amplitude, within  $\pm 2$  ppm of final frequency

 $\leq$  5 msec. (typ. 3 msec)

Phase Noise Spurious outputs  $\leq$  -120 dBc/Hz at 1 kHz  $\leq$  -80dBc



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Frequency Adjustment	
Range	≥ ± 5.0 ppm, ≤ ± 10.0 ppm
Control Voltage	1.5 ± 1.0 V
Linearity	$\leq 2\%$
Slope	Positive
Input resistance	$\geq$ 100k $\Omega$
Modulation Response	DC to $\geq$ 100 Hz less than ±1dB Ripple.

### **Environmental:**

Storage Temperature Rang	Je: -55 to +125°C
Vibration	IEC 60068-2-6 Test Fc Procedure B4, 10-60Hz 1.5mm displacement, at 98.1 ms <sup>-2</sup> , 30 minutes in each of three mutually perpendicular axes at 1 octave per minute
Shock	IEC 60068-2-27 Test Ea, 980ms <sup>-2</sup> acceleration for 6ms duration, 3 shocks in each direction along three mutually perpendicular axes
Soldering	SMD product suitable for Convection Reflow soldering. Peak temperature 260°C. Maximum time above 220°C, 60 secs.
Solderability	MIL-STD-202, Method 208, Category 3
Marking	Laser Marked