CRYSTAL OSCILLATOR (Programmable)

OUTPUT: CMOS

SG-8018 series

• Frequency range: 0.67 MHz ~ 170 MHz (1 ppm Step)

• Supply voltage : 1.62 V ~ 3.63 V

Function : Output enable (OE) or Standby (ST)
 Frequency tolerance : ±50 ppm (-40 °C ~ +105 °C)
 Include frequency aging(+25 °C, 10 years)

• Package : 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 (mm)

• PLL technology to enable short lead time

• Available field oscillator programmer "SG-Writer II"





Product Number (please contact us) SG-8018CA: X1G005571xxxx00 SG-8018CB: X1G005581xxxx00 SG-8018CE: X1G005591xxxx00 SG-8018CG: X1G005601xxxx00









CG CE CB CA 2.5 x 2.0mm 3.2 x 2.5mm 5.0 x 3.2mm 7.0 x 5.0mm

Specifications (characteristics)

Item Supply voltage Output frequency range Storage temperature Operating temperature Frequency tolerance* Current consumption Output disable current	Symbol V _{CC} f ₀ T_stg T_use f_tol l _{CC} l_dis l_std	1.62 V ~ 1.98 V 3.2 mA Max. 2.7 r 5.5 mA Max.	V Typ. 1.98 V ~ 2.20 V 0.67 MHz -40 °C ~ -40 °C ~	2.50 V Typ. 2.20 V ~ 2.80 V ~ 170 MHz +125 °C +105 °C 0 × 10 6 3.4 mA Max. 2.9 mA Typ. 6.7 mA Max.	3.30 V Typ. 2.70 V ~ 3.63 V 3.5 mA Max.	Storage as single p	-			
Output frequency range Storage temperature Operating temperature Frequency tolerance ¹¹ Current consumption	fo T_stg T_use f_tol lcc	3.2 mA Max. 2.7 r 5.5 mA Max. 4.7 r	1.98 V ~ 2.20 V 0.67 MHz -40 °C ~ -40 °C ~ J: ±50 3.3 mA Max. mA Typ. 5.8 mA Max.	2.20 V ~ 2.80 V ~ 170 MHz +125 °C +105 °C 0 × 10 ⁶ 3.4 mA Max. 2.9 mA Typ.	2.70 V ~ 3.63 V	T_use = -40 °C ~ +	-			
Output frequency range Storage temperature Operating temperature Frequency tolerance ¹¹ Current consumption	fo T_stg T_use f_tol lcc	3.2 mA Max. 2.7 r 5.5 mA Max. 4.7 r	0.67 MHz -40 °C ~ -40 °C ~ J: ±50 3.3 mA Max. mA Typ. 5.8 mA Max.	~ 170 MHz +125 °C +105 °C 0 × 10 ⁶ 3.4 mA Max. 2.9 mA Typ.		T_use = -40 °C ~ +	-			
Storage temperature Operating temperature Frequency tolerance* Current consumption	T_stg T_use f_tol lcc I_dis	2.7 r 5.5 mA Max. 4.7 r	-40 °C ~ -40 °C ~ J: ±50 3.3 mA Max. mA Typ. 5.8 mA Max.	+125 °C +105 °C) × 10 ⁻⁶ 3.4 mA Max. 2.9 mA Typ.	3.5 mA Max.	T_use = -40 °C ~ +	-			
Operating temperature Frequency tolerance *1 Current consumption	T_use f_tol lcc I_dis	2.7 r 5.5 mA Max. 4.7 r	-40 °C ~ J: ±50 3.3 mA Max. mA Typ. 5.8 mA Max.	+105 °C 0 × 10 ⁻⁶ 3.4 mA Max. 2.9 mA Typ.	3.5 mA Max.	T_use = -40 °C ~ +	-			
Frequency tolerance*1 Current consumption	f_tol	2.7 r 5.5 mA Max. 4.7 r	J: ±50 3.3 mA Max. mA Typ. 5.8 mA Max.	3.4 mA Max. 2.9 mA Typ.	3.5 mA Max.		- 105 °C			
Current consumption	I _{CC}	2.7 r 5.5 mA Max. 4.7 r	3.3 mA Max. mA Typ. 5.8 mA Max.	3.4 mA Max. 2.9 mA Typ.	3.5 mA Max.		105 °C			
·	I_dis	2.7 r 5.5 mA Max. 4.7 r	mA Typ. 5.8 mA Max.	2.9 mA Typ.	3.5 mA Max.	T use - +105 °C	T_use = -40 °C ~ +105 °C			
·	I_dis	5.5 mA Max. 4.7 r	5.8 mA Max.			_	No load, f _O = 20 MHz			
·	I_dis	4.7 r		67 mA May	3.0 mA Typ.	T_use = +25 °C	140 1040, 16 = 20 141112			
Output disable current	_		mA Tvn	U. I IIIA IVIAX.	8.1 mA Max.	T_use = +105 °C	No load, f _O = 170 MHz			
Output disable current	_	3.2 m A May		5.7 mA Typ.	6.8 mA Typ.	T_use = +25 °C	170 1040, 18 = 170 101112			
	I_std	J.Z IIIA IVIAX.	3.2 mA Max.	3.3 mA Max.	3.5 mA Max.	OE = GND, f _O = 170	MHz			
Standby current	1_510	0.9 µA Max.	1.0 µA Max.	1.5 µA Max.	2.5 μA Max.	T_use = +105 °C	ST = GND			
Startuby Current		0.3 μA Typ.	0.4 μA Typ.	0.5 μA Typ.	1.1 μA Typ.	T_use = +25 °C	ST = GND			
Symmetry	SYM		45 %	~ 55 %		50 % V _{CC} Level				
						I _{OH} /I _{OL} Conditions	[mA]			
	.,					Rise/Fall time	V _{CC} *A *B *C *D			
	V _{OH}	90 % V _{CC} Min.			Default (f _O > 40 MHz), Fast					
Output voltage							I _{OL} 2.5 3.5 4.0 5.0			
(DC characteristics)						Default (f _O ≤ 40 MHz)	I _{OH} -1.5 -2.0 -2.5 -3.0 I _{OI} 1.5 2.0 2.5 3.0			
(0.	I _{OH} -1.0 -1.5 -2.0 -2.5			
	V _{OL}	10 % V _{CC} Max.			I _{OL} 1.0 1.5 2.0 2.5					
						*A: 1.62 V ~ 1.98 V, *B: 1.98 V ~ 2.20 V,				
Output land annelition	1 01400		45.	- T M	*C: 2.20	0 V ~ 2.80 V, *D: 2.70 V ~ 3.63 V				
Output load condition	L_CMOS			oF Max.		-				
Input voltage	V _{IH}		70 % \		OE or ST					
	V _{IL}		30 % V	cc Max.						
Defau	+		3.0 ns Max.			f _O > 40 MHz				
Rise and Fall			6.0 ns Max.				20 % - 80 % V _{CC.}			
time Fast	tr/tf		3.0	ns Max.		f _O = 0.67 MHz ~ 170	0 MHz L_CMOS = 15 pF			
Slow		10.0 ns Max.				f _O = 0.67 MHz ~ 20	MHz			
Disable Time t_stp		1 μs Max.				Measured from the time OE or ST pin crosses 30 % $V_{\rm CC}$				
Enable Time t_sta		1 μs Max.				Measured from the time OE pin crosses 70 % V _{CC}				
Resume Time	t_res		3 r	ns Max.			time ST pin crosses 70 % V _{CC}			
Start-up time t_str		3 ms Max.				Measured from the time V _{CC} reaches its rated minimum value, 1.62 V				
Frequency aging		This is included in frequency tolerance specification.				+25 °C, 10 years				

^{*1} Frequency tolerance includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, load drift and aging (+25 °C, 10 years).

Pin description

Pin	Name	I/O type	Function				
	OE	Input Output enable		High: Specified frequency output from OUT pin			
1				Low: Out pin is low (weak pull down), only output driver is disabled. High: Specified frequency output from OUT pin			
'	ST Input		Standby	Low: Out pin is low (weak pull down),			
				Device goes to standby mode. Supply current reduces to the least as I_std.			
2	GND	Power	Ground				
3	OUT	Output	Clock output				
4	V _{CC}	Power	Power supply				

Product Name

$\underline{\mathsf{SG-8018CG}}\,\underline{\mathsf{170.000000MHz}}\,\,\underline{\mathsf{T}}\,\,\,\underline{\mathsf{J}}\,\,\,\underline{\mathsf{H}}\,\,\,\underline{\mathsf{P}}\,\,\underline{\mathsf{A}}$

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45678

CA: 7.0 mm x 5.0 mm

CB: 5.0 mm x 3.2 mm CE: 3.2 mm x 2.5 mm

CG: 2.5 mm x 2.0 mm

②Package type

 Supply voltage T: 1.8 V ~ 3.3 V Typ.

J: 50 x 10⁻⁶

@Operating temperature H: -40 °C ~ +105 °C

® Rise/Fall time A: Default B: Fast

①Model, ②Package type,

③Frequency, ④Supply voltage,

⑤Frequency tolerance, ⑥Operating temperature,

Tunction,

Rise/Fall time

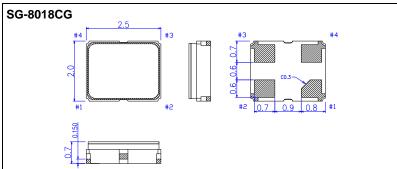
⑤Frequency tolerance ⑦Function

> P: Output Enable S: Standby

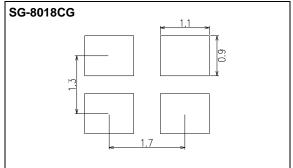
C: Slow

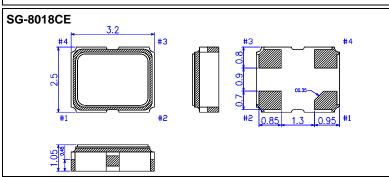
External dimensions

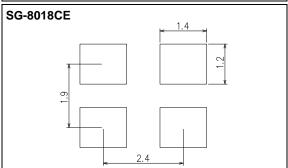
(Unit: mm)

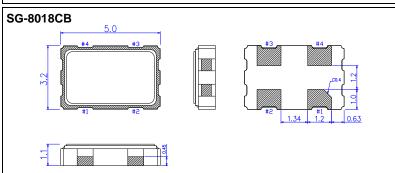


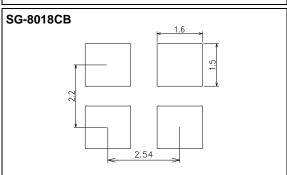
Footprint (Recommended) (Unit: mm)

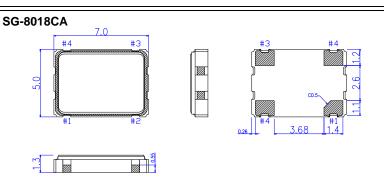


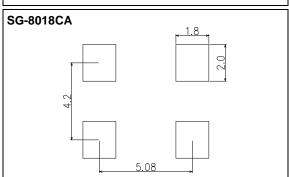










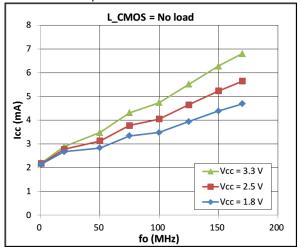


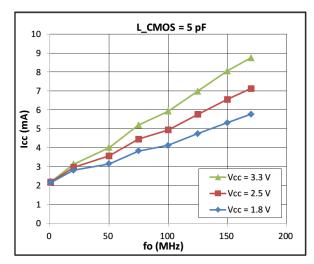
In order to achieve optimum jitter performance, the 0.1 μF capacitor between V_{CC} and GND should be placed. It is also recommended that the capacitors are placed on the device side of the PCB, as close to the device as possible and connected together with short wiring pattern.

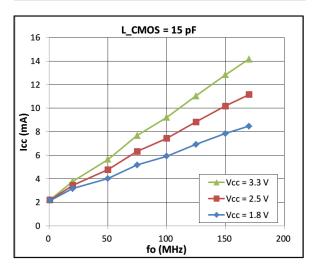
Crys

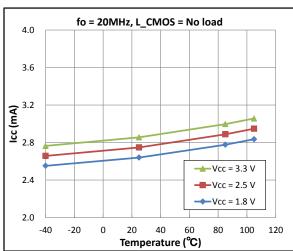
Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15pF)

Current Consumption

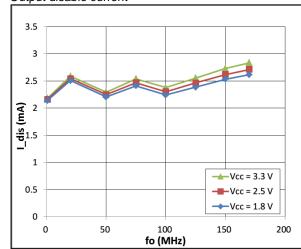


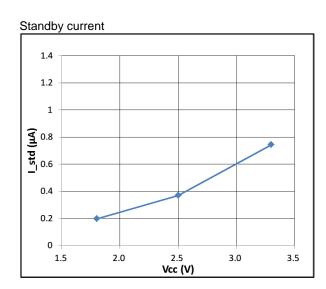






Output disable current



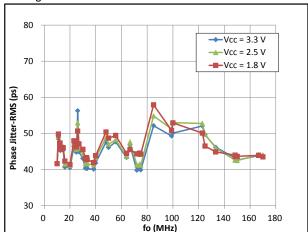


■ Notes:

Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15pF)

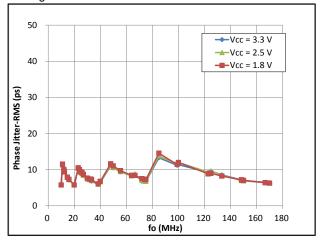
Phase Jitter RMS

(Integration bandwidth 12 k-20 MHz)

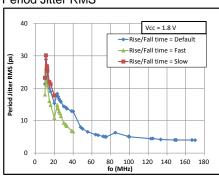


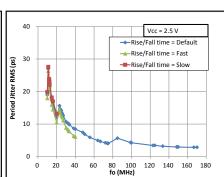
Phase Jitter RMS

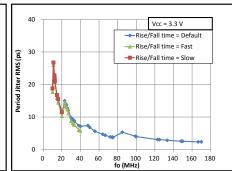
(Integration bandwidth 1.8 M-20 MHz)



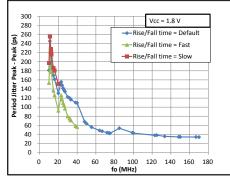
Period Jitter RMS

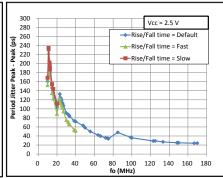


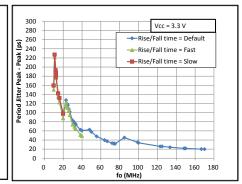




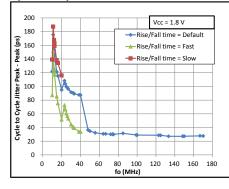
Period Jitter Peak-Peak

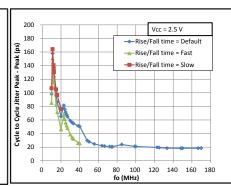


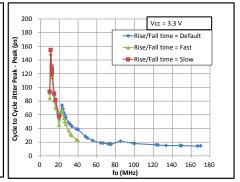




Cycle-to-Cycle Jitter Peak-Peak





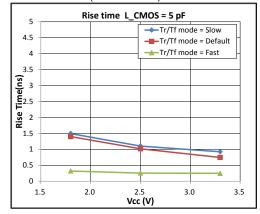


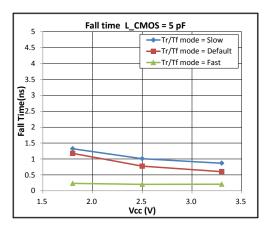
■ Notes:

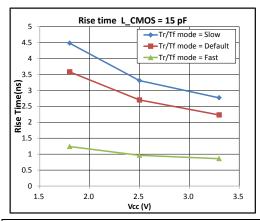
Crysta

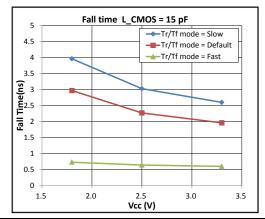
Specification Graph (Typical supplemental specification. Unless otherwise specified T_use = 25 °C, L_CMOS = 15pF)

Rise/Fall Time (fo = 20 MHz)





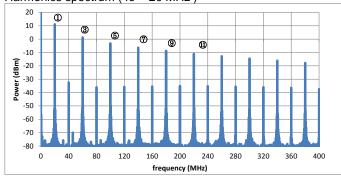


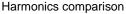


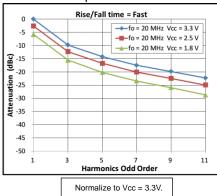
■Notes:

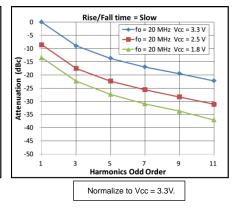
frequency	slow	default	fast	
0.67 M – 20 M	See Slow	See Default	See Fast	
20 M – 40 M	-	See Default	See Fast	
40 M – 170 M	_	See Fast	See Fast	

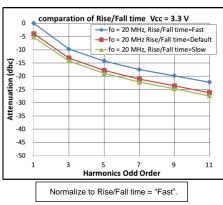
Harmonics spectrum (fo = 20 MHz)











■Notes:



ESD Rating

Test items	Breakdown voltage
Human Body Model (HBM)	2000V
Machine Model (MM)	250V
Charged Device Model (CDM)	750V

Device Marking (Standard specification)

Model	Factory Programmed Part Marking	Field Programmable Part Marking (Blank Samples)
SG-8018CG	Frequency Product code 170. A2 OA23DM 1pin mark Lot No.	A2 OA23DM Lot No.
SG-8018CE	Frequency 170.0A2 o A23DM Lot No.	A2 o A23DM 1pin mark Lot No.
SG-8018CB	Product code 170.0 A2 A23DM 1pin mark Lot No.	A2 Product code A23DM 1pin mark Lot No.
SG-8018CA	Frequency 170.00 A2 A23DM 1pin mark Lot No.	A2 O A23DM Lot No.

Simulation Model

• IBIS Model is available upon request. Please contact us. Information Required: Oscillator operating condition (i.e. Power Supply, Rise/Fall Time, Temperature)

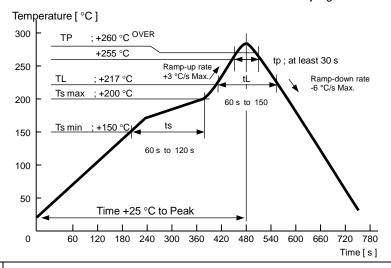


Device Material & Environmental Information

Model	Package	# of	Reference	Terminal	Terminal	Complies	Pb	MSL	Peak
	Dimensions	Pins	Weight	Material	Plating	With EU	Free	Rating	Temp.
			(Typ.)			RoHS			(Max)
SG-8018CG	2.5 x 2.0 x 0.7 mm	4	13 mg	W	Au	Yes	Yes	1	260°C
SG-8018CE	3.2 x 2.5 x 1.0 mm	4	25 mg	W	Au	Yes	Yes	1	260°C
SG-8018CB	5.0 x 3.2 x 1.1 mm	4	51 mg	W	Au	Yes	Yes	1	260°C
SG-8018CA	7.0 x 5.0 x 1.3 mm	4	143 mg	W	Au	Yes	Yes	1	260°C

SMD products Reflow profile(example)

The availability of the heat resistance for reflow conditions of JEDEC-STD-020D.01 is judged individually. Please inquire.





Pb free.



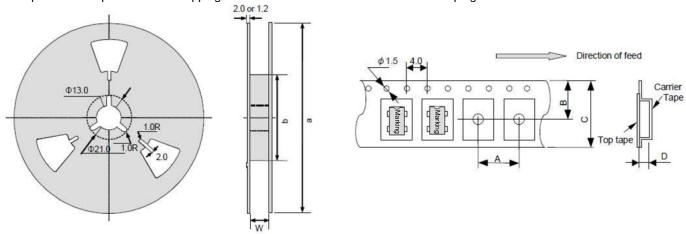
- Complies with EU RoHS directive.
 - About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)

Standard Packing Specification

SMD products are packed in the shipping carton as below table in accordance with taping standards EIA-481 and IEC-60286



Standard Packing Quantity & Dimension(Unit mm)

	Quantity	Reel Dimension			Car	Direction of			
Model	(pcs/Reel)	а	b	W	А	В	С	D	Feed (L= Left Direction)
SG-8018CG	3000	Ф180	Ф60	9	4	5.25	8	1.15	L
SG-8018CE	2000	Ф180	Ф60	9	4	5.25	8	1.4	L
SG-8018CB	1000	Ф180	Ф60	13	8	7.25	12	1.4	L
SG-8018CA	1000	Ф254	Ф100	17.5	8	9.25	16	2.3	L

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs.

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



- ► Complies with EU RoHS directive.
 - *About the products without the Pb-free mark.

 Contains Pb in products exempted by EU RoHS directive.

 (Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



 \blacktriangleright Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc).

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