

# 2.5 x 2.0mm CMOS LOW VOLTAGE 1.0V

## SMD OSCILLATOR

Pb-free & RoHS compliant

KFY

"Preliminary"

### FREQUENCY STABILITY

MODEL	FREQUENCY STABILITY
KF1Y1A	± 100ppm/-10 ~ +70
KF2Y1A	± 50ppm/-10 ~ +70
KF3Y1A	± 25ppm/-10 ~ +70
KF1Y1R	± 100ppm/-40 ~ +85
KF2Y1R	± 50ppm/-40 ~ +85
KF3Y1R	± 25ppm/-40 ~ +85

### OPERATING CONDITIONS

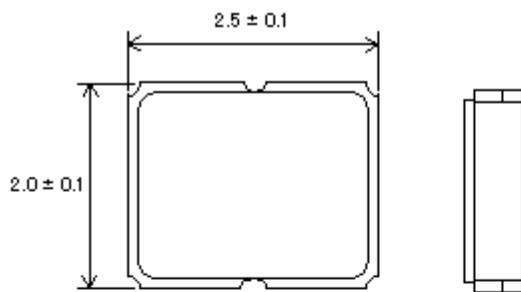
Operating Temperature	-10 ~ +70 , -40 ~ +85
Storage Temperature	-55 ~ +125
Supply Voltage (Vdd)	+1.0V ± 5%

### ELECTRICAL CHARACTERISTICS (Ta=-10 ~ +70 , Vdd=1.0V, CL=15pF)

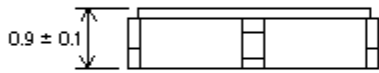
PARAMETERS	CONDITIONS	SPECIFICATIONS
Frequency Range (MHz)		1.800 ~ 50.000
Input Current (Idd)	1.800 ~ 32.100MHz 32.100+ ~ 50.000MHz	2.5mA Max. 3.5mA Max.
Frequency Stability	All Conditions (Note)	± 25ppm ~ ± 100ppm
Symmetry	@50%Vdd	40/60%
Output Voltage (Vol)	"0" Level	20%Vdd Max.
Output Voltage (Voh)	"1" Level	80%Vdd Max.
Rise Time (Tr)	20% ~ 80%Vdd 1.800 ~ 32.100MHz 32.100+ ~ 50.000MHz	5.0ns Max. 3.5ns Max.
Fall Time (Tf)	80% ~ 20% Vdd 1.800 ~ 32.100MHz 32.100+ ~ 50.000MHz	5.0ns Max. 3.5ns Max.
Output Current (Iol)	"0" Level	2mA Min.
Output Current (Ioh)	"1" Level	2mA Min.
Stand-by Current		5µA Max.
Output Load	HCMOS	15pF
Start-up Time	0.0V to Vdd	5ms Max.

Note: Inclusive of 25 tolerance, operating temperature change, supply voltage change and load change.

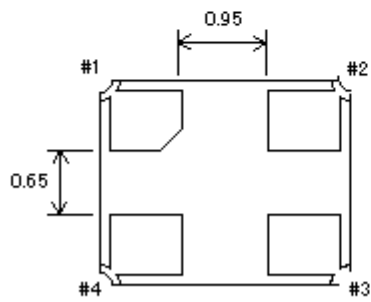
### DIMENSIONS (mm)



ENABLE/DISABLE FUNCTION	
Control (Pin #1)	OUTPUT (Pin #3)
Open	Active
"1" ( $V_{IH} \geq 70\%V_{DD}$ )	Active
"0" ( $V_{IL} \leq 30\%V_{DD}$ )	High Z



Pin Connections	
#1	E/D
#2	GND
#3	OUT
#4	$V_{DD}$



RECOMMENDED  
SOLDER PAD LAYOUT

