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5x7,5mm SMT CRYSTAL-OSCILLATORS
NMSOJ3 3,3V 30pF

FREQUENZSTABILITÄT *FREQUENCY STABILITY*	
Modell *Model*	
NM1SOJ3	±100ppm/-10~+70°C
NM2SOJ3	±50ppm/-10~+70°C
NM3SOJ3	±25ppm/-10~+70°C
NM4SOJ3	±20ppm/-10~+70°C
NM1SOJ3R	±100ppm/-40~+85°C
NM2SOJ3R	±50ppm/-40~+85°C
NM3SOJ3R	±25ppm/-40~+85°C

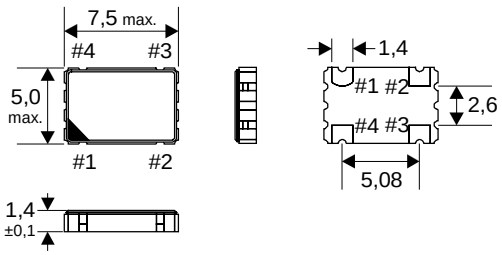
BETRIEBSBEDINGUNGEN *OPERATING CONDITIONS*	
Betriebstemperatur *operating temp.*	-10~+70°C, -40~+85°C
Lagertemperatur *storage temperature*	-55~+125°C
Betriebsspannung V_{DD} *supply voltage*	+3,3V ±0,3V
Feuchteempfindlichkeit *MSL*	1

Elektrische Daten *electrical characteristics*
T_a = 25°C, V_{DD} = 3,3 V, C_L = 15 pF

Parameter ***parameter***		**Bedingungen** ***conditions***	**Frequenzbereich** ***frequ. range***	**Spezifikationen** ***specifications***
max. Stromaufnahme *max. input current*	I_{DD}		1,8000$^+$ ~ 80,000 MHz	38 mA
Frequenzstabilität *frequency stability*		über alles *) all conditions *)	1,8000$^+$ ~ 80,000 MHz	±20 ppm ~ ±100 ppm
Tastverhältnis *symmetry*		@50% V_{DD}	1,8000$^+$ ~ 80,000 MHz	45/55 %
Ausgangsspannung *output voltage*	V_{OL} V_{OH}	"0" level "1" level	1,8000$^+$ ~ 80,000 MHz	10% V_{DD} max. 90% V_{DD} min.
Anstiegszeit max. *rise time max.*	T_R	10% - 90% V_{DD}	1,8000$^+$ ~ 80,000 MHz	6 ns
Abfallzeit max. *fall time max.*	T_F	90% - 10% V_{DD}	1,8000$^+$ ~ 80,000 MHz	6 ns
Ausgangsstrom min. *output current min.*	I_{OL} I_{OH}	"0" level "1" level	1,8000$^+$ ~ 80,000 MHz	2 mA
Ruhestrom max. *standby current max.*		$V_{IL} \leq$ 30% V_{DD}	1,8000$^+$ ~ 80,000 MHz	10 µA
max. Belastbarkeit *max. driving ability*		TTL	1,8000$^+$ ~ 80,000 MHz	5 N-TTL
		HCMOS	1,8000$^+$ ~ 80,000 MHz	30 pF
Startzeit max. *start-up time max.*		0,0 - 3,3 V	1,8000$^+$ ~ 80,000 MHz	10 ms

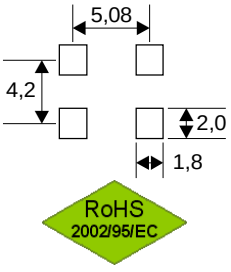
*) Anmerkung: inkl. Abgleichtoleranz, Temperaturgang, Spannungs- und Laständerung, Alterung, Schock und Vibration
note: incl. frequency and temperature tolerance, supply voltage and load change, aging, shock and vibration

Abmessungen in mm
dimensions in mm



lead-free/RoHS-conformal

empfohlenes Layout
recommended solder pad layout



Anschlußbelegung *pin connections*	
#1	E/D
#2	GND
#3	OUT
#4	V_{DD}

Funktionstabelle (f. NMSOL3) *enable /disable function*	
INH (pin #1)	output (pin #3)
open	active
"1" ($V_{IH} \geq$ 70% V_{DD})	active
"0" ($V_{IL} \leq$ 30% V_{DD})	high Z