



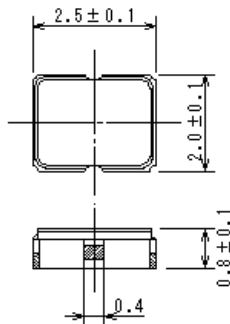
2,0x2,5mm LOW CURRENT OSCILLATOR SRP1 SRN1 SRS1 SRW1

FREQUENZSTABILITÄT FREQUENCY STABILITY		BETRIEBSBEDINGUNGEN OPERATING CONDITIONS		
Modell <i>Model</i>				
SR1P1B SR1N1B SR1S1B SR1W1B	±100ppm/-20~+70°C	Betriebstemperatur <i>operating temp.</i>		-20~+70°C, -40~+85°C
SR2P1B SR2N1B SR2S1B SR2W1B	±50ppm/-20~+70°C	Lagertemperatur <i>storage temperature</i>		-55~+100°C
SR8P1B SR8N1B SR8S1B SR8W1B	±30ppm/-20~+70°C	Betriebsspannung V_{DD} <i>supply voltage</i>	SRP1	+3,3V ±5%
SR1P1R SR1N1R SR1S1R SR1W1R	±100ppm/-40~+85°C		SRN1	+3,0V ±5%
SR2P1R SR2N1R SR2S1R SR2W1R	±50ppm/-40~+85°C		SRS1	+2,5V ±5%
SR8P1R SR8N1R SR8S1R SR8W1R	±30ppm/-40~+85°C		SRW1	+1,8V ±5%

Elektrische Daten <i>electrical characteristics</i>					
Parameter <i>parameter</i>	Bedingungen <i>conditions</i>	Spezifikation <i>specification</i>			
		SRP1	SRN1	SRS1	SRW1
Stromaufnahme <i>max. input current</i>	I_{DD} 0,750 ~ 19,999 20,000 ~ 39,999 40,000 ~ 50,000	6,0mA Max. 7,0mA Max. 8,0mA Max.	5,5mA Max. 6,5mA Max. 7,5mA Max.	4,5mA Max. 5,5mA Max. 6,5mA Max.	2,5mA Max. 3,0mA Max. 3,5mA Max.
Frequenzbereich <i>Frequency range</i>	über alles *) all conditions *)	0,750 ~ 50,000			
Frequenzstabilität <i>frequency stability</i>		±30ppm ~ ±100ppm			
max. Belastbarkeit <i>max. driving ability</i>	CMOS	15pF Max.			
Ausgangsspannung <i>output voltage</i>	V_{OL} V_{OH}	"0" level "1" level 90% VDD Min. 10% VDD Max.			
Anstiegszeit max. <i>rise time max.</i>	T_R	10% ~ 90% V_{DD} 10ns Max.			
Abfallzeit max. <i>fall time max.</i>	T_F	10% ~ 90% V_{DD} 10ns Max.			
Tastverhältnis <i>symmetry</i>	@ 50% V_{DD}	45/55%		40/60%	45/55%

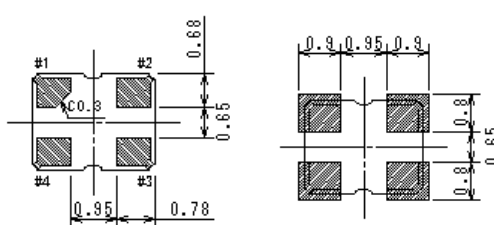
*) Anmerkung: inkl. Abgleichtoleranz, Temperaturgang, Spannungs- und Laständerung, Alterung, Schock und Vibration
note: incl. frequency and temperature tolerance, supply voltage and load change, aging, shock and vibration

Abmessungen in mm
dimensions in mm



lead-free/RoHS-conformal

empfohlenes Layout
recommended solder pad layout



Anschlußbelegung
pin connections

#1	E/D
#2	GND
#3	OUTPUT
#4	+ V_{DD}

Funktionstabelle
enable/disable function

INH (pin #1)	output (pin #3)
open	active
"1" ($V_{IH} \geq 80\% V_{DD}$)	active
"0" ($V_{IL} \leq 20\% V_{DD}$)	high Z