

CXOMK/CXOMKHG OSCILLATOR

200 kHz to 200 MHz High Stability, High Shock Crystal Oscillator

DESCRIPTION

Statek's CXOMK/CXOMKHG series oscillators consist of a Statek miniature quartz crystal and a CMOS/TTL compatible hybrid circuit in a ceramic package. Utilizing the latest advancements in production technology, the CXOMK/CXOMKHG oscillators are capable of achieving tight frequency calibration tolerance and high stability over wide temperature ranges.

FEATURES

- High shock resistance (HG version)
- CMOS and TTL compatible
- Optional Output Enable/Disable with Tri-State
- Low EMI emission
- Full military testing available
- Hermetically sealed ceramic package

APPLICATIONS

Military & Aerospace

- Smart Munitions
- Cockpit Systems
- Navigation

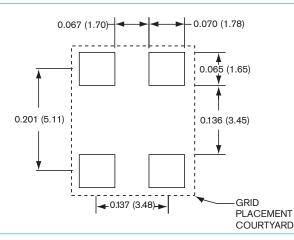
Industrial, Computer & Communications

- Industrial Controls
- Instrumentation
- Microprocessor Clocks

Medical

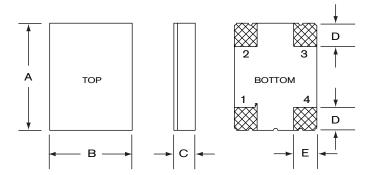
Infusion Pumps

SUGGESTED LAND PATTERN





DIMENSIONS



	TYPICAL		MAXIMUM		
DIM	inches	mm	inches	mm	
А	0.256	6.50	0.263	6.68	
В	0.197	5.00	0.204	5.18	
C (SM1) C (SM3/SM5)	0.055 0.060	1.34 1.52	0.060 0.065	1.52 1.65	
D	0.055	1.40	0.065	1.65	
E	0.060	1.52	0.070	1.78	

PIN CONNECTIONS

- 1. Enable/Disable (E or T) or not connected (N)
- 2. Ground
- 3. Output
- 4. V_{DD}

10210 Rev B



SPECIFICATIONS

Specifications are typical at 25°C unless otherwise noted. Specifications are subject to change without notice. Tighter specifications available. Please contact factory.

Supply Voltage ¹ Calibration Tolerance ² Frequency Stability Over Temperature ³	0.9 V to 5.0 V ±10% ±30 ppm ±50 ppm to ±15 (Commercial) ±100 ppm to ±30 (Industrial) ±100 ppm to ±40 (Military)		
		3.3V	5.0V
Supply Current (Typical)	10 MHz	2mA	4 mA
	24 MHz	4mA	8 mA
	30 MHz	6mA	10 mA
	40 MHz	8mA	12 mA
	50 MHz	10mA	14 mA
Output Load (CMOS)⁴	15 pF		
Start-up Time	5 ms MA	Х	
Rise/Fall Time	6 ns MAX	<	
Duty Cycle	40% MIN	I, 60% MA	X
Aging, first year	10 ppm N	MAX	
Shock, survival		0	ms, $1/_2$ sine ms, $1/_2$ sine
Vibration, survival⁵	20 g, 10-	-2,000 Hz	swept sine
Operating Temp Ranges	-40°C to -55°C to	+85°C +125°C	(Industrial) (Military)
1. Voltages available: 0.9 V, 1.8 V	/, 2.5 V, 3.0 V,	3.3 V and 5.0	V.

 Voltages available: 0.9 V, 1.8 V, 2.5 V, 3.0 V, 3.3 V and 5.0 V. For others, contact factory. Not all voltages are available for all frequencies.

2. Tighter tolerances available.

3. Does not include calibration tolerance. Tighter tolerances may be available.

4. Higher CMOS loads and TTL loads available. Contact factory.

5. Per MIL-STD-202G, Method 204D, Condition D. Random vibration testing also available.

Note: All parameters are measured at ambient temperature with a 10 $\text{M}\Omega\text{, }$ 15 pF load.

PACKAGING OPTIONS

CXOMK/CXOMKHG - Tray Pack

- 16 mm tape, 7" or 13" reels Per EIA 418 (see Tape and Reel data sheet 10109)

ABSOLUTE MAXIMUM RATINGS

Supply Voltage V_DD-0.5 V to 7.0 V*Storage Temperature-55°C to +125°CMaximum Process Temperature260°C for 20 seconds

*The supply voltage range is -0.5 V to +4.0 V for some products. Contact Factory.

ENABLE/DISABLE OPTIONS (E/T/N)

Statek offers three enable/disable options: E, T, and N. Both the E-version and T-version have Tri-State outputs and differ in whether the oscillator continues to run internally when the output is put into the high Z state: it stops in the E-version and continues to run in the T-version. So, the Eversion offers very low current consumption when the oscillator is disabled and the T-version offers very fast output recovery when the oscillator is re-enabled. The N-version does not have PIN 1 connected internally and so has no enable/disable capability. The following table summarizes the three options.

COMPARISON OF ENABLE/DISABLE OPTIONS E AND T

	E	т				
When enabled (PIN 1 is high*)						
Output	Freq. output	Freq. output				
Oscillator	Oscillates	Oscillates				
Current consumption	Normal	Normal				
When disabled (PIN 1 is low)						
Output	High Z state	High Z state				
Oscillator	Stops	Oscillates				
Current consumption	Very low	Lower than normal				
When re-enabled (PIN 1 changes from low to high)						
Output recovery	Delayed	Immediate				

*When PIN 1 is allowed to float, it is held high by an internal pull-up resistor.

HOW TO ORDER CXOMK/CXOMKHG SURFACE MOUNT CRYSTAL OSCILLATORS

