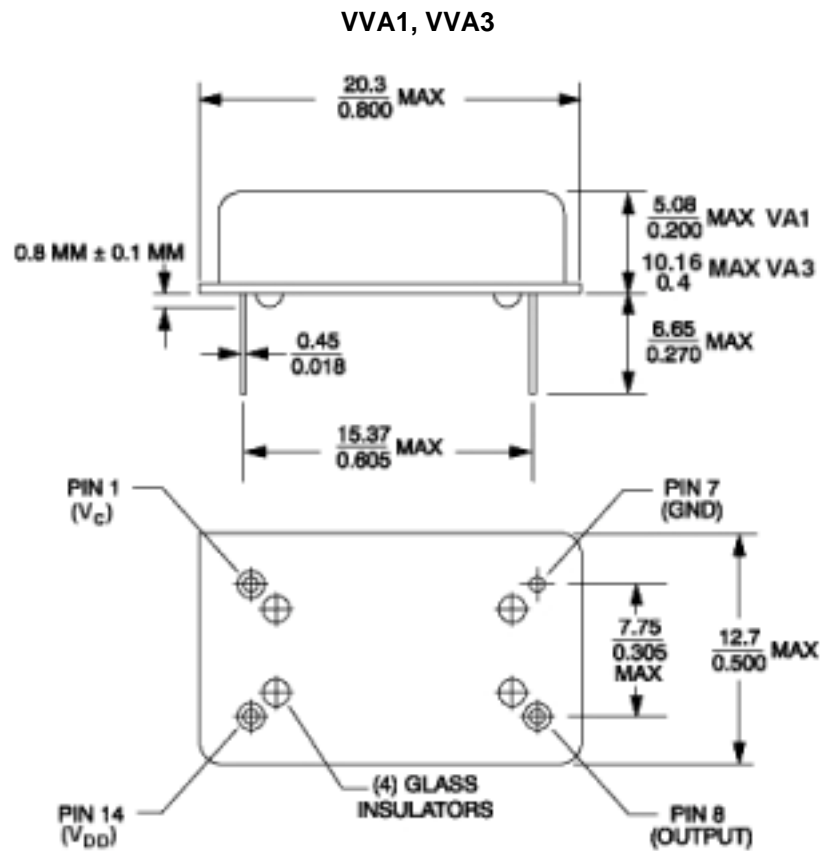


VVA1, VVA2, VVA3 Full Size VCXO

Package Option	A1 = 0.8" x 0.5" x 0.2" 14 pin DIP A2 = 0.8" x 0.5" x 0.2" 14 pin Gull Leaded Surface Mount A3 = 0.8" x 0.5" x 0.4" 14 pin DIP
Frequency Range	1.0 to 40 MHz (Fundamental <80ps/pk-pk Jitter) 40 to 160 MHz uses a low jitter internal multiplier IC (<150ps/pk-pk Jitter) which improves aging and pullability but will also increase phase noise
Electrical Options	A = +5.0 Vdc \pm 20% Linearity B = +3.3 Vdc \pm 20% Linearity E = +5.0 Vdc \pm 10% Linearity F = +3.3 Vdc \pm 10% Linearity
Voltage Control Range Slope Positive	1 = \pm 50 PPM Minimum 2 = \pm 100 PPM Minimum 3 = \pm 150 PPM Minimum 4 = \pm 200 PPM Minimum
Stability Options (Inclusive of Operating Temperature, Supply Voltage and Load)	A = \pm 100 PPM 0C to +70C B = \pm 50 PPM 0C to +70C C = \pm 100 PPM -40C to +85C D = \pm 50 PPM -40C to +85C E = \pm 25 PPM 0C to +70C
Tight Stability Options	G = \pm 20 PPM 0C to +70C A3 Package H = \pm 10 PPM 0C to +70C A3 Package I = \pm 20 PPM -40C to +85C A3 Package
Calibration Tolerance	\pm 25 PPM at +25 \pm 2C at Center Voltage
Duty Cycle	40/60
Start-Up	10 ms Maximum
Aging	<5.0 PPM/First year at +40C dynamic A1 and A2 Package <2.0 PPM/First year at +40C dynamic A3 Package
Load	15 pF HCMOS or 5 TTL Maximum
Current	20 mA Maximum \leq 20 MHz 40 mA Maximum \leq 30MHz 50 mA Maximum >30MHz

Environmental and Mechanical Compliance

Parameter	Conditions
Mechanical Shock	MIL-STD-883, 2002.3 B
Mechanical Vibration	MIL-STD-883, 2007.1 B
Lead Solderability	MIL-STD-883, 2003.5
Gross Leak	MIL-STD-883, 1014.7
Fine Leak	MIL-STD-883, 1014.7
Storage Temperature	-55C to 125C



VVA2

